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Using complementation and resequencing to minimize transitions

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 Fujitsu Labs. of America Inc., USA

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Abstract

In (Murgai et al., 1997) the following problem was addressed: given a set of **data** words or messages to be transmitted over a **bus** such that the sequence (order) in which they are transmitted is irrelevant, determine the optimum sequence that minimizes the total number of transitions on the bus. Stan and Burleson (1994) presented the **bus-invert** method as a means of encoding words for reducing I/O power, in which a word may be inverted and then transmitted if doing so reduces the number of transitions. In this paper, we combine the two paradigms into one—that of sequencing words under the **bus-invert** scheme for the minimum transitions, i.e., words can be **complemented**, reordered and then transmitted. We prove that this problem **DOP1-Data Ordering Problem with Inversion** is NP-complete. We present a polynomial-time approximation algorithm to solve DOP1 that comes within a factor of 1.5 from the optimum. Experimental results show that, on average, the solutions generated by our algorithm were within 4.4% of the optimum, and that resequencing along with complementation leads to 34.4% reduction in switching activity.

Index Terms

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Controlled Indexing

computational complexity, graph theory, minimisation, scheduling, switching theory, system buses

Non-controlled Indexing

DOP1, Data Ordering Problem with Inversion, NP-complete, bus-invert method, complementation, encoding, optimum sequence, polynomial-time approximation algorithm, resequencing, system bus, transition minimization

Author Keywords

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<u>L2</u> count\$3 near10 data near10 bus	6764	<u>L2</u>
<u>L1</u> 710/305,100,34,65;365/49,63,190,233.5,205;713/502;327/518;326/35,86;370/464,476;712/220.ccls.	13372	<u>L1</u>

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<u>L6</u> complementing same data same bus	200	<u>L6</u>
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<u>L4</u> 11 and L3	2	<u>L4</u>
<u>L3</u> complementing same data same bus same (threshold or limit)	5	<u>L3</u>
<u>L2</u> complementing near10 data near10 bus near10 (threshold or limit)	1	<u>L2</u>
<u>L1</u> count\$3 near10 data near10 bus	6764	<u>L1</u>

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11	<input type="checkbox"/>	<input type="checkbox"/>	US 5019720	19910528	19	Static type semiconductor memory	365/208	365/207
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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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1. Sign bit reduction encoding for low power applications

Saneei, M.; Afzali-Kusha, A.; Navabi, Z.;
Design Automation Conference, 2005. Proceedings. 42nd
13-17 June 2005 Page(s):214 - 217

[AbstractPlus](#) | Full Text: [PDE](#)(236 KB) IEEE CNF

2. A low power technique based on sign bit reduction

Saneei, M.; Afzali-Kusha, A.; Navabi, Z.;
Microelectronics, 2004. ICM 2004 Proceedings. The 16th International Conference on
6-8 Dec. 2004 Page(s):497 - 500
Digital Object Identifier 10.1109/ICM.2004.1434708

[AbstractPlus](#) | Full Text: [PDE](#)(207 KB) IEEE CNF

3. Number representations for reducing data bus power dissipation

Sacha, J.R.; Irwin, M.J.;
Signals, Systems & Computers, 1998. Conference Record of the Thirty-Second Asilomar Conference on
Volume 1, 1-4 Nov. 1998 Page(s):213 - 217 vol.1
Digital Object Identifier 10.1109/ACSSC.1998.750856

[AbstractPlus](#) | Full Text: [PDE](#)(408 KB) IEEE CNF

4. A graphical simulation tool for teaching microprocessors architecture and assembly language

Ramirez, J.M.; Navarro, A.; Gallardo, V.; Baez-Lopez, D.;
Frontiers in Education Conference, 1997. 27th Annual Conference. 'Teaching and Learning in an Era of Change'.
Proceedings.
Volume 2, 5-8 Nov. 1997 Page(s):701 vol.2
Digital Object Identifier 10.1109/FIE.1997.635902

[AbstractPlus](#) | Full Text: [PDE](#)(52 KB) IEEE CNF

5. A layout synthesis methodology for array-type analog blocks

Van der Plas, G.; Vandenbussche, J.; Gielen, G.G.E.; Sansen, W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 21, Issue 6, June 2002 Page(s):645 - 661
Digital Object Identifier 10.1109/TCAD.2002.1004309

[AbstractPlus](#) | [References](#) | Full Text: [PDE](#)(875 KB) IEEE JNL

6. Improve - HDL - a DO-254 formal property checker used for design and verification of avionics protocol controllers

Dellacherie, S.; Burgaud, L.; di Crescenzo, P.;
Digital Avionics Systems Conference, 2003. DASC '03. The 22nd
Volume 1, 12-16 Oct. 2003 Page(s):1.A.1 - 1.1-8 vol.1
Digital Object Identifier 10.1109/DASC.2003.1245801

[AbstractPlus](#) | Full Text: [PDF](#)(572 KB) IEEE CNF



7. Field device integration

Simon, R.; Diedrich, C.; Riedl, M.; Thron, M.;
Industrial Electronics, 2001. Proceedings. ISIE 2001. IEEE International Symposium on
Volume 1, 12-16 June 2001 Page(s):150 - 155 vol.1
Digital Object Identifier 10.1109/ISIE.2001.931772

[AbstractPlus](#) | Full Text: [PDF](#)(505 KB) IEEE CNF



8. Fault signal filtering for improving fault section estimation

Chang, C.S.; Kerk, S.G.;
Power Engineering Society Winter Meeting, 2000. IEEE
Volume 4, 23-27 Jan. 2000 Page(s):2545 - 2550 vol.4
Digital Object Identifier 10.1109/PESW.2000.847214

[AbstractPlus](#) | Full Text: [PDF](#)(504 KB) IEEE CNF



9. Using complementation and resequencing to minimize transitions

Murgai, R.; Fujita, M.; Oliveria, A.;
Design Automation Conference, 1998. Proceedings
15-19 Jun 1998 Page(s):694 - 697

[AbstractPlus](#) | Full Text: [PDF](#)(428 KB) IEEE CNF



10. Two dimensional optical data links

Novotny, R.A.; Wojcik, M.J.; Beckman, M.G.; Hinterlong, S.J.; Lentine, A.L.;
Electronic Components and Technology Conference, 1993. Proceedings., 43rd
1-4 June 1993 Page(s):790 - 794
Digital Object Identifier 10.1109/ECTC.1993.346759

[AbstractPlus](#) | Full Text: [PDF](#)(548 KB) IEEE CNF



11. The provision of on street passenger information via real time passenger information; a case study of Dublin

Caulfield, B.; O'Mahony, M.M.;
Road Transport Information and Control, 2004. RTIC 2004. 12th IEEE International Conference on
20-22 April 2004 Page(s):1 - 10

[AbstractPlus](#) | Full Text: [PDF](#)(673 KB) IEEE CNF



12. A multi-core SoC design for advanced image and video compression

Dehnhardt, A.; Kulaczewski, M.B.; Friebe, L.; Moch, S.; Pirsch, P.; Stolberg, H.-J.; Reuter, C.;
Acoustics, Speech, and Signal Processing, 2005. Proceedings. (ICASSP '05). IEEE International Conference on
Volume 5, 18-23 March 2005 Page(s):v/665 - v/668 Vol. 5
Digital Object Identifier 10.1109/ICASSP.2005.1416391

[AbstractPlus](#) | Full Text: [PDF](#)(346 KB) IEEE CNF



13. OPC DX and Industrial Ethernet glues fieldbus together

Xiaohong Hao; Shunhong Hou;
Control, Automation, Robotics and Vision Conference, 2004. ICARCV 2004 8th
Volume 1, 6-9 Dec. 2004 Page(s):562 - 567 Vol. 1
Digital Object Identifier 10.1109/ICARCV.2004.1468888

[AbstractPlus](#) | Full Text: [PDF](#)(267 KB) IEEE CNF



14. Description of the StarLight mission and spacecraft concept

Deiningner, W.D.; Weiss, M.A.; Wiemer, D.J.; Hoffman, C.N.; Cleven, G.C.; Patel, K.C.; Linfield, R.P.; Livesay, L.L.;
Aerospace Conference, 2001, IEEE Proceedings.
Volume 1, 10-17 March 2001 Page(s):1/187 - 1/197 vol.1
Digital Object Identifier 10.1109/AERO.2001.931709

[AbstractPlus](#) | Full Text: [PDF](#)(1188 KB) IEEE CNF



15. Field device integration

Neumann, P.; Simon, R.; Diedrich, C.; Riedl, M.;
Emerging Technologies and Factory Automation, 2001. Proceedings. 2001 8th IEEE International Conference on
Volume 2, 15-18 Oct. 2001 Page(s):63 - 68 vol.2
Digital Object Identifier 10.1109/ETFA.2001.997672

[AbstractPlus](#) | Full Text: [PDF](#)(559 KB) [IEEE CNF](#)



16. Static property checking using ATPG vs. BDD techniques

Chung-Yang Huang; Bwolen Yang; Huan-Chih Tsai; Kwang-Ting Cheng;

Test Conference, 2000. Proceedings. International

3-5 Oct. 2000 Page(s):309 - 316

Digital Object Identifier 10.1109/TEST.2000.894219

[AbstractPlus](#) | Full Text: [PDF](#)(696 KB) [IEEE CNF](#)



17. Systems commonality: integrated avionics for the MH-47E and the MH-60K

Smith, C.;

Digital Avionics Systems Conference, 1991. Proceedings., IEEE/AIAA 10th

14-17 Oct. 1991 Page(s):526 - 532

Digital Object Identifier 10.1109/DASC.1991.177220

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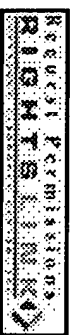
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Using complementation and resequencing to minimize transitions

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